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# Cirrus logic cs4398 datasheet

Part Number : CS4398 Function : 120 dB, 192 kHz Multi-Bit DAC with Volume Control Package : 28-pin TSSOP Type Manufacturers : Cirrus Logic Live stockFeel free to contact us for more information.Technical DocumentRoHS Certificate of Compliance CS4398MCLK (MHz)SampleModeRate(sample-MCLKDIV2MCLKDIV3(KHz)rate range)MCLK Ratio256x384x512x768x1024x1152x328.192012.288016.384024.576032.768036.8640Single-Speed(32 to 50 kHz)44.111.289616.934422.579233.868845.1584-4812.288018.432024.576036.864049.1520-MCLK Ratio128x192x256x384x512x-648.192012.288016.384024.576032.7680-Double-Speed(50 to 100 kHz)88.211.289616.934422.579233.868845.1584-9612.288018.432024.576036.864049.1520-MCLK Ratio64x\*96x128x192x256x-176.411.2896\*16.934422.579233.868845.1584-Quad-Speed(100 to 200 kHz)19212.2880\*18.432024.576036.864049.1520-These modes are only available in Control Port mode by setting the appropriate MCLKDIV bit.\* This MCLK ratio limits the audio word length to 16 bits; see Table 1 on page 21Table 2. Common Clock Frequencies4.6Stand-alone Mode SettingsIn Stand-Alone mode (also referred to as "Hardware mode") the device is configured using the M0 throughM3 pins. These pins must be connected to either the VLC supply or ground.The Interface format is set bypins M0 and M1. The sample rate range/oversampling mode (Single/Double/Quad-Speed mode) and de-emphasis are set by pins M2 and M3. The settings can be found in Tables 3 and 4.M10011M00101DescriptionLeft-Justified, up to 24-bit dataPS, up to 24-bit dataRight-Justified, 16-bit DataRight-Justified, 24-bit DataFormat0123Figure3455Table 3. Digital Interface Format, Stand-Alone Mode OptionsM30011M20101DescriptionSingle-Speed without De-Emphasis (32 to 50 kHz sample rates)Single-Speed with 44.1 kHz De-Emphasis; see Figure 17 on page 30Double-Speed (50 to 100 kHz sample rates)Quad-Speed (100 to 200 kHz sample rates)Table 4. Mode Selection, Stand-Alone Mode OptionsThe following features are always enabled in Stand-Alone mode: Auto-mute on zero data, Auto MUTE C po-larity detect, ramp volume from mute to 0dB by 1/8th dB steps every LRCK (soft ramp) after reset or clockmode change, and the fast roll-off interpolation filter is used.The following features are not available in Stand-Alone mode: DSD mode, Right-Justified 20- and 18-bit se-rial audio interfaces, MCLK divide-by-2 and MCLK divide-by-3 (allows 1024 and 1152 clock ratios), slow roll-off interpolation filter, volume control, ATAPI mixing, 48 kHz and 32 kHz de-emphasis, and all other featuresenabled by registers that are not mentioned above.22DS568F1, gauru:34ValueArchitectureDelta SigmaResolution24 bitConversion Rate216ksp/sNumber of DAC Channels2Mounting TypeSurface MountPackage TypeTSSOPPin Count28Digital Interface TypeSerialOutput PolarityBipolarVoltage ReferenceExternal, InternalPower Supply TypeSingleTypical Single Supply Voltage5 VMinimum Operating Temperature-10 °CMaximum Operating Temperature+70 °CDimensions9.7 x 4.4 x 0.9mm The CS4398 is a low power, 120 dB stereo digital/analog converter (DAC) featuring digital de-emphasis, half dB step-size volume control, ATAPI channel mixing, and selectable fast and slow roll off digital interpolation filters. This stage is followed by an over sampled multi-bit Delta Sigma modulator, which includes mismatch shaping technology that eliminates distortion due to capacitor mismatch, and a multi-element switched capacitor stage and low-pass filter with differential analog outputs. A Direct Stream Digital® (DSD) processor allows for volume control and 50 kHz on-chip filtering without an intermediate decimation stage, plus an optional path for direct DSD conversion by directly using the multi-element switched capacitor array. The IC accepts DSD audio and 32 kHz to 200 kHz data sample rates, has selectable digital filters and delivers excellent sound quality. Cirrus Rocks® Cirrus® Engineered to Rock® Innovation that Rocks® Low-latency digital filterDirect Stream Digital mode (SACD)Low-clock-jitter sensitivityDifferential analog outputsControl output for external muting Channel(s) Out 2 Resolution (bits) 24 Dynamic Range (dB) 120 THD+N (dB) -107 Sample Rate (kHz) 30-216 Analog Power Supply (V) 5 Digital Power Supply (V) 1.8-5.0 Operational Power Consumption (mW) Output Level (Vrms) 2.3 Package 28 TSSOP For more information about Cirrus Logic's Hi-Fi product line, visit the Hi-Fi Audio page. Datasheet begin, distributors inventory © 2004-2021 digchip.com Our company TME has over 1000 employees, who provide expert support at each stage of the ordering process. Our offer includes 400,000 electronic components from 1200 producers. Since 1990, we have been expanding our operations dynamically and increasing our global potential. Every day, we send 5000 shipments and we ensure that they arrive in the shortest time available.

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